CLAIM LISTING

No claims have been amended, canceled, or added. A Claim Listing is provided as a courtesy.

1. (Previously Presented) A voltage control circuit comprising:

a bandgap device to produce a reference voltage based on a desired power level; and

a decision device to receive the reference voltage from the bandgap device and to receive a voltage signal from a power source, the decision device to determine whether the voltage signal received from the power source corresponds to the desired power level,

wherein the voltage signal is coupled to power a plurality of distributed oscillators, and

wherein the plurality of distributed oscillators are coupled to drive a clock distribution network asynchronously with respect to an external clock.

2. (Original) The circuit of claim 1, wherein the decision device comprises a comparing device to compare values associated with the reference voltage and the voltage signal, and to output a result signal based on the comparison.

3. (Original) The circuit of claim 2, wherein the decision device further comprises a decision mechanism to receive the result signal output from the comparing device and to make a decision regarding operation of the power source.

4. (Original) The circuit of claim 2, wherein the decision comprises determining that the voltage signal is not at the desired power level.

5. (Canceled).

6. (Canceled).

7. (Original) The circuit of claim 1, wherein the voltage control circuit is provided within a core of an integrated circuit.

8. (Original) The circuit of claim 7, wherein the voltage signal is provided by voltage regulating device located external to the core.

9. (Canceled).

10. (Canceled).

11. (Canceled).

12. (Canceled).

13. (Previously Presented) An apparatus comprising:

a clock distribution network;

at least one oscillator coupled to the clock distribution network so as to provide a clock signal on the clock distribution network; and

a power control circuit to control power applied to the at least one oscillator the power control circuit including:

a bandgap device to produce a reference voltage based on a desired power level; and

a decision device to receive the reference voltage from the bandgap device and to receive a voltage signal from a power source external to the apparatus, the decision device to determine whether the voltage signal received from the power source corresponds to the desired power level,

wherein the voltage signal is coupled to power a plurality of distributed oscillators, and

wherein the plurality of distributed oscillators are coupled to drive the clock distribution network asynchronously with respect to an external clock.

14. (Original) The apparatus of claim 13, wherein the decision device comprises a comparing device to compare values associated with the reference voltage and the voltage signal, and to output a result signal based on the comparison.

15. (Original) The apparatus of claim 14, wherein the decision device further comprises a decision mechanism to receive the result signal output from the comparing device and to make a decision regarding operation of the power source.

16. (Original) The apparatus of claim 14, wherein the decision comprises determining that the voltage signal is not at the desired power level.

17. (Canceled).

18. (Canceled).

19. (Original) The apparatus of claim 13, wherein the power control circuit is provided within a core of an integrated circuit.

20. (Original) The apparatus of claim 19, wherein the voltage signal is provided by a voltage regulating device located external to the core.

21. (Canceled).

22. (Canceled).

23. (Canceled).

24. (Canceled).

25. (Previously Presented) An apparatus comprising:

a processor core having a voltage control circuit and at least one oscillator to produce core clock signals; and

a power source located external to the core to provide power for the at least one oscillator based on a signal received from the processor core,

the voltage control circuit including:

a first device to produce a reference signal indicative of a desired power level to power the at least one oscillator; and

a second device to receive the reference signal and to receive a signal from the power source, the second device to determine whether the signal received from the power source corresponds to the desired power level,

wherein the signal received from the power source is coupled to power a plurality of distributed oscillators, and

wherein the plurality of distributed oscillators are coupled to drive a clock distribution network asynchronously with respect to an external clock.

26. (Original) The apparatus of claim 25, wherein the second device comprises a comparing device to compare values associated with the reference signal and the signal received from the power source, and to output a result signal based on the comparison.

27. (Original) The apparatus of claim 26, wherein the second device further comprises a decision mechanism to receive the result signal output from the comparing device and to make a decision regarding operation of the power source.

28. (Original) The apparatus of claim 27, wherein the decision comprises determining that the signal from the power source is not at the desired power level.

29. (Canceled).

30. (Canceled).

31. (Canceled).

32. (Canceled).

33. (Canceled).

34. (Canceled).

35. (Canceled).

36. (Canceled).

37. (Canceled).

38. (Canceled).

39. (Canceled).

40. (Canceled).

41. (Previously Presented) An apparatus comprising:

a processor core having a voltage control circuit and at least one oscillator t produce core clock signals; and

a power source located external to the core to provide power for the at least one oscillator based on a signal received from the processor core,

the voltage control circuit including:

a first device to produce a reference signal indicative of a desired power level to power the at least one oscillator;

a second device to receive the reference signal and to receive a signal from the power source, the second device to determine whether the signal received from the power source corresponds to the desired power level; and a device to provide a first code and a second code based on the desired power level, the reference signal being produced based on the first code.

42. (Previously Presented) The apparatus of claim 41, wherein the signal received from the power source is based on the second code.

43. (Previously Presented) The apparatus of claim 41, wherein the first code is identical to the second code.

44. (Previously Presented) The apparatus of claim 41, wherein the first code is different than the second code.